



PATENT  
Customer No. 22,852  
Attorney Docket No. 8245.0027-00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jun Dong KIM et al.

Application No.: 09/892,878

Filed: June 28, 2001

For: METHOD FOR FORMING GATE  
ELECTRODES IN A  
SEMICONDUCTOR DEVICE  
USING FORMED FINE  
PATTERNS

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) Group Art Unit: 2814  
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) Examiner: S. Rao  
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TECHNOLOGY CENTER 2800

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**PETITION FOR EXTENSION OF TIME**

Applicants hereby petition for a two-month extension of time to reply to the Office Action of December 3, 2002. A fee of \$410.00 is enclosed.

Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

By: Donald D. Min  
Donald D. Min  
Reg. No. 47,796

Dated: May 5, 2003

05/06/2003 JADD01  
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